

Customer No.: 31561  
Application No.: 10/711,863  
Docket No.: 14098-US-PA

**REMARKS**

Claims 11 and 13 have been amended to include subject matter fabricated by the method of claim 1, while claim 14 is currently canceled hereby.

**Claim Rejections – 35 U.S.C. § 102**

The Office Action rejected claims 11, 13-15 under 35 U.S.C. 102(b) as being anticipated by US 3,354,543 (Lawrence).

In response thereto, Applicants submit that such a method as set forth in allowable claim 1 is neither taught, disclosed, nor suggested by Lawrence, or any of the other cited references, taken alone or in combination.

Similarly, applicant submits that such a circuit board fabricated by the method of allowable claim 1 or claim 3 is neither taught, disclosed, nor suggested by Lawrence, or any of the other cited references, taken alone or in combination, and thus should be allowed.

Lawrence teaches an electroplating process configuring a conductive copper layer over a surface of the through hole (Column 6, lines 1-15; FIG. 2(e)). However, Lawrence fails to teach the structure and process of “a conductive column formed by a conductive material filling with the through hole from a position where the blind ends of the first and second blind holes connect with each other and extend toward two ends of the through hole (Emphasis

Customer No.: 31561  
Application No.: 10/711,863  
Docket No.: 14098-US-PA

added)” as set forth in claims 11 and 13. Therefore, claims 11 and 13 and the dependent claim 15 are submitted to be patentable over Lawrence, or any of the other cited references, taken alone or in combination, and thus should be allowed.

Claims 11 and 13 are rejected under 35 U.S.C. 102(b) as being anticipated by US 6,114,240 (Akram).

In response thereto, Applicant has amended claim 11 and 13 to include subject matter fabricated by the method of claim 1, and hereby otherwise traverses this rejection.

Applicants submit that such a method as set forth in allowable claim 1 is neither taught, disclosed, nor suggested by Akram or any of the other cited references, taken alone or in combination.

Similarly, applicant submits that such a circuit board fabricated by the method of allowable claim 1 or claim 3 is neither taught, disclosed, nor suggested by Akram, or any of the other cited references, taken alone or in combination, and thus should be allowed.

Akram teaches a process steps for fabricating the conductive vias in FIG. 2A-2D. However, Lawrence fails to teach the structure and process of **“a conductive column formed by a conductive material filling with the through hole from a position where the blind ends of the first and second blind holes connect with each other and extends towards two ends of the through hole (Emphasis added)”** as set forth in claims 11 and 13. Therefore, claims 11 and

Customer No.: 31561  
Application No.: 10/711,863  
Docket No.: 14098-US-PA

13 and the dependent claim 15 are submitted to be patentable over Akarm, or any of the other cited references, taken alone or in combination, and thus should be allowed.

Customer No.: 31561  
Application No.: 10/711,863  
Docket No.: 14098-US-PA

### CONCLUSION

For at least the foregoing reasons, it is believed that the pending claims 11, 13, 15, 17 and 18 are in proper condition for allowance and an action to such effect is earnestly solicited. If the Examiner believes that a telephone conference would expedite the examination of the above-identified patent application, the Examiner is invited to call the undersigned.

Date :

Feb. 1, 2007

Respectfully submitted,

Belinda Lee

Belinda Lee

Registration No.: 46,863

Jianq Chyun Intellectual Property Office  
7<sup>th</sup> Floor-1, No. 100  
Roosevelt Road, Section 2  
Taipei, 100  
Taiwan  
Tel: 011-886-2-2369-2800  
Fax: 011-886-2-2369-7233  
Email: [belinda@jciigroup.com.tw](mailto:belinda@jciigroup.com.tw)  
[usa@jciigroup.com.tw](mailto:usa@jciigroup.com.tw)